
This is a book on VLSI design, and the objective of this book is to teach you how to design digital circuits. The book has two major parts; we teach you how to use a standard hardware description language (HDL) as well as understanding the basics of device-level circuit design. You can access content like these through our project where we provide free online lectures, video tutorials and challenges. We also provide free PDF copies of each chapter which you can download from our website. If you are really interested in learning more about how digital circuits work, then do get this book for your personal library. It would surely help you in building better digital circuits for computers/smartphones/solar panels etc. The main method of explaining the concepts is through examples. At the end of each chapter, you will find a series of exercises that will further enhance your understanding of that chapter. This book is divided into seven chapters. The book covers basic topics in digital circuit design like number representations, logic gates, standard cell library modeling, sequential circuits, VHDL language constructs and design process, HDL floor planning and logic optimization techniques. This book also covers various design methodologies like multi-vendor library based methodology, multi-targeting approach for performance optimization etc. Design examples are given to clarify all these concepts in detail. This book also covers slightly advanced topics like custom cell library modeling, design for testability and design of complex arithmetic circuits. You will also find a chapter on project-based learning which will help you in understanding the entire design process from the beginning to the end. This chapter will cover how to model a simple adder using various HDL constructs, how to test this adder, and then how to optimize the adder. In addition to that, we have provided a chapter on challenges that you can solve with help from this book. VLSI Design Book is designed by authors Akshay Verma and Amritesh Dubey who are working as VLSI Design Engineers in Intel India Pvt Ltd. Both the authors are part of Intel's mobile SoC design team. "This article uses material from the VLSI Design Book , which is licensed under the Creative Commons Attribution-Noncommercial-Share Alike 3.0 Unported License but not under the GFDL." This book is free to download in pdf format from our website along with further resources like videos, articles, project tutorials etc.

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